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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/695,187	10/28/2003	William C. Weigler	IS01207AP	4286	
22917 7	7590 05/30/2006		EXAMINER ·		
MOTOROLA, INC.			JOHNSON, JONATHAN J		
1303 EAST AI IL01/3RD	LGONQUIN ROAD		ART UNIT PAPER NUMBER		
SCHAUMBURG, IL 60196			1725		
			DATE MAILED: 05/30/2006	DATE MAILED: 05/30/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	
	10/695,187	WEIGLER ET AL.	
Office Action Summary	Examiner	Art Unit	
	Jonathan Johnson	1725	•
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the	correspondence addr	ess
A SHORTENED STATUTORY PERIOD FOR REPI WHICHEVER IS LONGER, FROM THE MAILING [ - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the maili earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION  .136(a). In no event, however, may a reply be the distribution of the community	N. mely filed  the mailing date of this come (ED (35 U.S.C. § 133).	
Status			
<ul> <li>1) Responsive to communication(s) filed on 18 / 2a) This action is FINAL.</li> <li>2b) Th</li> <li>3) Since this application is in condition for allow closed in accordance with the practice under</li> </ul>	is action is non-final. ance except for formal matters, pr		nerits is
closed in accordance with the practice under	Ex parte Quayre, 1955 C.D. 11, 4	33 O.G. 213.	
Disposition of Claims			
4) ⊠ Claim(s) 1-20 is/are pending in the application 4a) Of the above claim(s) 15-20 is/are withdra 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-14 is/are rejected. 7) □ Claim(s) is/are objected to. 8) ⊠ Claim(s) 1-20 are subject to restriction and/or	own from consideration.		
Application Papers			i
9) The specification is objected to by the Examin	ner ·		
10) The drawing(s) filed on is/are: a) ac		Examiner.	
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the corre			
Priority under 35 U.S.C. § 119			:
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of:  1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the pri application from the International Burea * See the attached detailed Office action for a list	nts have been received. nts have been received in Applicat ority documents have been receiv au (PCT Rule 17.2(a)).	tion No red in this National S	tage
Attachment(s)    Online   Notice of References Cited (PTO-892)	4) ☐ Interview Summary	y (PTO-413)	
Notice of Draftsperson's Patent Drawing Review (PTO-948)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/06)  Paper No(s)/Mail Date	Paper No(s)/Mail D	Date	152)

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### **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-7 and 9-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over US 3580462 (Reynolds) in view of US 5,143,272 (Carlomango). Reynolds teaches providing a sacrificial circuit substrate with a single pad (figure 3, items 20 and 22), a portion of each pad having a solder-wettable material disposed thereon (figure 3, items 20 and 22); placing the pad of the sacrificial circuit substrate in vertical proximity to the excess solder of the circuit substrate (Figure 3, item 18); heating the excess solder to a liquidous state (figure 4, item 26); wicking the excess solder vertically onto the pad of the sacrificial circuit substrate (figure 3, item 22); and lifting the sacrificial circuit substrate from the proximity of the circuit substrate while the solder is in a liquidous state (figure 5, item 28); wherein the circuit substrate is flexible and wherein the providing step includes providing a rigid sacrificial circuit substrate (figure 1, item 18); heating step includes using hot gas to reflow the excess solder (figure 3, item 26); the pad including are plated with a solder-wettable material used as the wicking material (figure 3, item 22) where all surfaces in contact with the solder is plated with the wicking material (figure 3, item 22); wherein the dimensions of the pads, vias and through-holes are configured to leave a residual amount of solder on the circuit substrate after the wicking and lifting steps (figure 2, item 14);

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applying flux to the sacrificial circuit substrate. Carlomango teaches the use of a vacuum suction device at each point of removing excess solder (Figure 1, item 20). It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the method of Reynolds to utilize a vacuum suction point at each solder land in order to further assist in removing of excess solder (see Carlomango abstract).

Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over US 3580462 (Reynolds) and US 5,143,272 (Carlomango) as applied to claim 1 above and further in view of JP 52-42447 (MATU). Matu teaches applying flux in order to remove impurities and reduce surface tension imparted to the solder (abstract). It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the method of Reynolds and Carlomango to utilize applying flux to the sacrificial circuit in order to reduce the impurities and the surface tension of the solder (Matu abstract).

Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over US 3580462 (Reynolds) and US 5,143,272 (Carlomango) as applied to claim 9 above and further in view of JP 52-42447 (MATU). Matu teaches applying flux in order to remove impurities and reduce surface tension imparted to the solder (abstract). It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the method of Reynolds and Carlomango to utilize applying flux to the sacrificial circuit in order to reduce the impurities and the surface tension of the solder (Matu abstract).

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## Response to Arguments

Applicant's declaration is improper. The declaration must include an acknowledgment by the declarant that willful false statements and the like are punishable by fine or imprisonment, or both (18 U.S.C. 1001) and may jeopardize the validity of the application or any patent issuing thereon. The declarant must set forth in the body of the declaration that all statements made of the declarant's own knowledge are true and that all statements made on information and belief are believed to be true.

Applicant's declaration used to show that "vacuum suction would not be utilized to perform removal of excess solder if damage to the circuit substrate is of concern [] as in Reynolds" is unpersuasive (page 8 of applicant's response). Applicant claims that the vacuum "may cause damage to the circuit board," however this is merely an opinion evidence with no underlying factual basis. See In re Chilowsky, 306 F.2d 908, 134 USPQ 515 (CCPA 1962). Applicant does not provide any test results showing how (or to what extent) the circuit board will be damaged. Furthermore, applicant does not explain how often the circuit board will be damaged as applicant only states that the vacuum "may cause damage." This could be meant by applicant to be less than 0.01% of the time.

The examiner notes that applicant has provided no data, nor has applicant met his burden of explaining the data in the declaration he offers as evidence of non-obviousness. Ex parte

Ishizaka, 24 USPQ2d 1621, 1624 (Bd. Pat. App. & Inter. 1992).

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#### Conclusion

This is a RCE of applicant's earlier Application No. 10/695187. All claims are drawn to the same invention claimed in the earlier application and could have been finally rejected on the grounds and art of record in the next Office action if they had been entered in the earlier application. Accordingly, **THIS ACTION IS MADE FINAL** even though it is a first action in this case. See MPEP § 706.07(b). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no, however, event will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jonathan Johnson whose telephone number is 571-272-1177. The examiner can normally be reached on M-Th 7:30 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Pat Ryan can be reached on 571-272-1292. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Jonathan Johnson Primary Examiner Art Unit 1725